

MICREX-View X

High-performance CPU

A 1 GHz dual-core processor has been adopted for the CPU, achieving high-speed control with minimum cycle of 1 ms. This allows both high-speed electrical machinery control and high-functional measurement control to be performed with a single unit.

The CPU module has adopted multi-processor configuration architecture. It also provides advanced communication performance with scheduling technology to perform network processing concurrently with arithmetic processing of application programs.

Large-capacity memory

The control station includes large-capacity memory consisting of 512K steps of program memory and 2,368K words of data memory. Up to 4,096 words (32 nodes) of I/O can be connected per station.

High-speed LAN

The CPU module contains a 1 Gbps Ethernet-based control LAN, equalized bus, and 1 Gbps/100 Mbps Ethernet-based I/O network. With CPU processing scheduling technology, these high-speed LANs have achieved higher communication performance.

Control LAN

The adoption of an FL-net Ver.3-compliant LAN allows high-speed and large-capacity data communication as well as the support for duplication.

Equalized bus

Large-capacity equalization technology enables high-speed transfer of large data, resulting in the achievement of full-range equalization of application data.

Network adapter bus

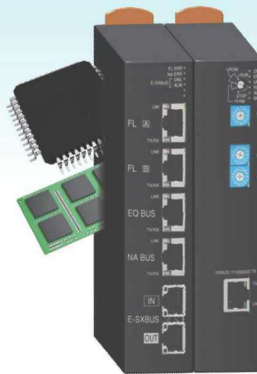
A high-speed LAN for a network adapter to be connected with Fuji Electric's legacy networks or other different networks.

E-SX bus

A high-speed I/O network supporting duplication.

High-reliability CPU

The CPU supports duplication. The adoption of ECC (with error detection/correction function) memory and the enhancement of RAS function have achieved a high-reliability control station.



Control station CPU module



Control station

Integrated Engineering Environment

